

# Pilot production of high efficient metal catalyzed textured diamond wire sawed mc-Si solar cells combined with nickel-copper plated front contact processing - SNEC 2017

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**Abstract:** This paper presents first results of combining two promising future technologies: i) metal catalyzed textured diamond-wire sawn (DWS) mc-Si wafering and ii) nickel-copper-silver (Ni/CuAg) plated front contact processing. Results of first optimizations of laser patterning, annealing and module string soldering are presented. A 60-cell module with a power output of 266 W has been successfully manufactured. The pseudo fill-factor (pFF) has been identified as the parameter with the highest potential for further improvements. A very promising approach is moving from an emitter profile that is mainly adapted to silver paste properties to an optimized emitter profile for Ni/Cu/Ag-plated contacts. The idea is to reduce the surface doping concentration (reduces Auger-recombination) and increase the junction depth (increased pFF and FF).

**Keywords:** Cu-plating; Light-induced plating; Diamond-wire saw, multi-crystalline texture, metal catalyzed texture

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## 1 Introduction

In the past decade, the PV industry undertook considerable efforts to make Si solar cells cost effective relative to conventional carbon emitting energy generation technologies (coal, gas etc.). This goal has been achieved for several regions in the world [1]. Now, due to production overcapacity, the continued and increased pressure to further reduce production costs is driving all promising innovations (both disruptive and evolutionary). One important innovation step in the past year was the replacement of slurry-wire-based wafer sawing technology with diamond-wire-based wafer sawing technology for cast multi-crystalline (mc-Si) wafers. Amongst other improvements, this was one main step to improve the cost advantage of mc-Si wafers compared to Cz-Si-wafers. On one hand, many solar cell producers are switching their production from mc- to Cz-wafer based silicon solar cells, because wafer costs per watt currently favor Cz-Si wafers [2]. On the other hand, different approaches are being investigated to cost-effectively texture diamond wire saw (DWS) mc-Si wafers. The common acidic  $\text{HNO}_3/\text{HF}$ -based texture process, optimized for slurry-wire sawn mc-Si wafers cannot be applied conveniently to DWS mc-Si wafers. One very promising approach is with so-called metal catalyzed texturing (MCT) technology. Its process details, electrical performance and feasibility for combination with Ni/Cu/Ag-plating is discussed in detail in the following sections of this paper.

A second, very promising approach to significantly reduce production costs is the substitution of cost-intensive screen-printed Ag-pastes with Ni/Cu/Ag-plating [3]. Furthermore, copper plating technology comes along with a potential to increase the efficiency by 0.1 to 0.3% absolute compared to the current screen-printing technology. Due to silver's roles as both a semi-precious investment metal and as an industrial commodity, silver paste prices are highly sensitive to volatile speculative trends in metals exchange markets, which in turn has the undesirable effect of shrinking the already-narrow margins of most solar cell manufactures. Replacing the Ag paste with a low-cost price-stable plated Ni/Cu/Ag stack would alleviate this cost pressure. Another important consideration is that due to the limited known-reserves of silver, it will certainly become a bottleneck in the scale-up of the PV technology as it seeks to play a more-significant role in the global power generation market [4]. Last, but not least, a copper-plated silicon solar cell would fit the broader commercial market's demand of abandoning lead, a toxic component of silver metallization pastes, from the device.

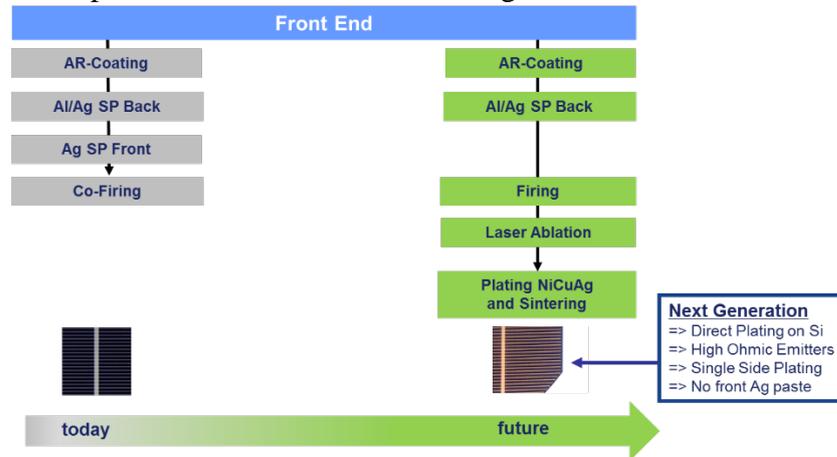
Thus, the two most cost-intensive factors in Si wafer-based solar cell production (wafer and metallization) are tackled by the following presented work.

## 2 Process and equipment

### 2.1 Solar cell processing

The following process flow was applied to all solar cells subsequently described herein: After obtaining wafers by diamond-wire sawing of the cast mc-Si ingot, an MCT surface texturing process as depicted in Fig. 2 was applied. The MCT process is discussed in more detail in section 2.2. Next, a shallow  $100\text{-}\Omega/\square$  phosphorous-doped emitter was formed via thermal diffusion of  $\text{POCl}_3$  (see Fig. 5, as formed). After this, PSG is etched from the cell surfaces and the junction is isolated by single-side silicon etch. Then, a  $\text{SiN}_x$  anti-reflection coating was deposited by tube PECVD on the front-side. On the rear-side, Al-paste (for Al-BSF formation)

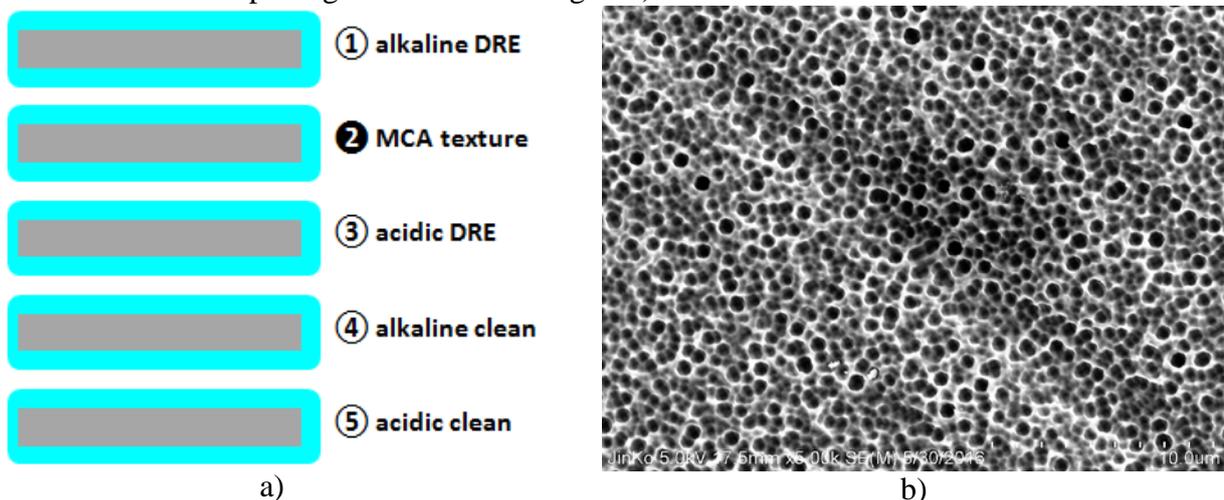
and silver pads (for cell inter-connection) was printed and fired for all Ni/Cu/Ag contacted solar cells. For the reference cells, an Ag-paste was screen-printed on the front side before firing. This comparison of metallization process flows is illustrated in Fig.1.



**Fig 1:** Process flow overview for conventional solar cells with screen-printed silver front-side contact metallization (left) and solar cells with laser-patterned, plated Ni/Cu/Ag contact metallization, and sintering (right).

## 2.2 Metal-catalyzed texturing of diamond-wire sawn multi-crystalline Si-wafers

The process flow for metal-catalyzed texturing of diamond-wire sawn multi-crystalline Si-wafers is shown in Figure 2a. First, the DWS mc-wafers are treated with an alkaline damage-removal etch (DRE), followed by the actual MCT process, which can be sub-divided into a metal deposition step and an acidic etching step [5,6]. Next, an acidic damage-removal step is applied, followed by an alkaline clean and then an acidic clean. In-between all process steps, a rinse needs to be conducted. Application of this process chain to DWS mc-Si wafers results in surface morphologies as shown in Fig. 2 b).

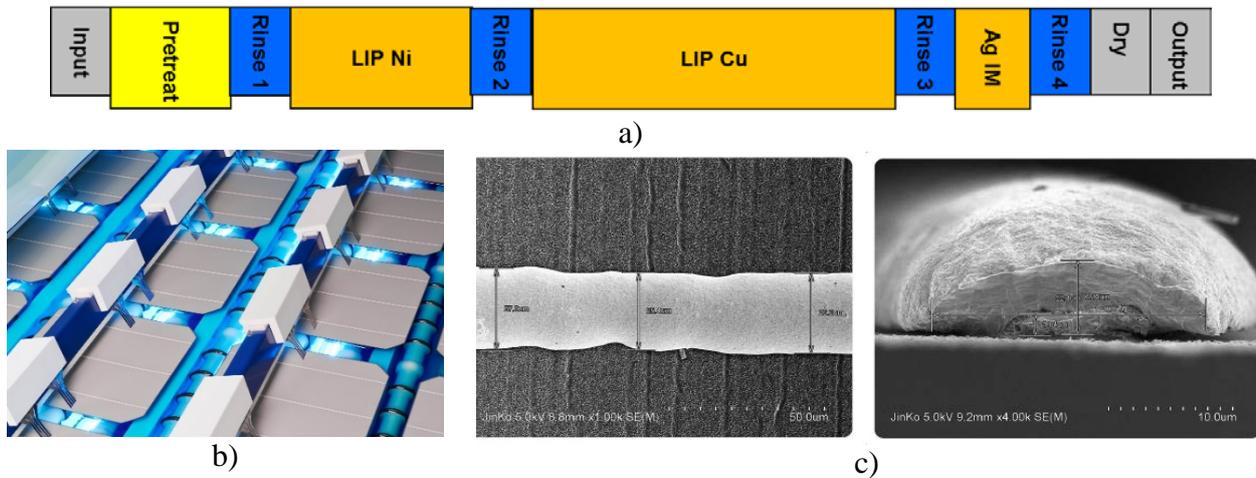


**Fig 2:** a) process overview for the MCT process, b) representative image of MCT-textured DSW mc-Si wafer surface.

## 2.3 Ni/Cu/Ag plating process

In order to deposit Ni/Cu/Ag-plated contacts, the contact pattern needs to be formed by selective UV-ps laser ablation of the front PECVD SiNx:H layer. The subsequent in-line plating relies on single-sided wet chemical processing in order to protect the rear contacts of the solar cells. The in-line plating process starts with a chemical pre-treatment of the laser-opened areas,

to assure plating will start on the exposed emitter surface where intended. Nickel (Ni) is plated by light-induced plating (LIP). A Cu layer is then plated on top of Ni, to give the contacts sufficient conductivity (see Fig. 3b) and c)). At the end of the plating sequence, single-sided immersion plating of Ag caps the Cu layer. This very thin (100-nm, or less) Ag layer prevents Cu oxidation and facilitates soldering of interconnection ribbons to the metal stack (see Fig. 3a). The laser step was performed on a Innolas equipment and all plating steps are performed with RENA-manufactured in-line equipment in conjunction with MacDermid-Enthone-produced plating chemistry. Intermediate rinsing and drying steps are applied in-between all process baths. In a last processing step, the complete metal stack is thermally annealed in an inert atmosphere. During this step, the adhesion of the metal stack to Si is improved and the contact resistance and line resistance are reduced via appropriate nickel-silicide formation at the nickel-silicon interface [7].



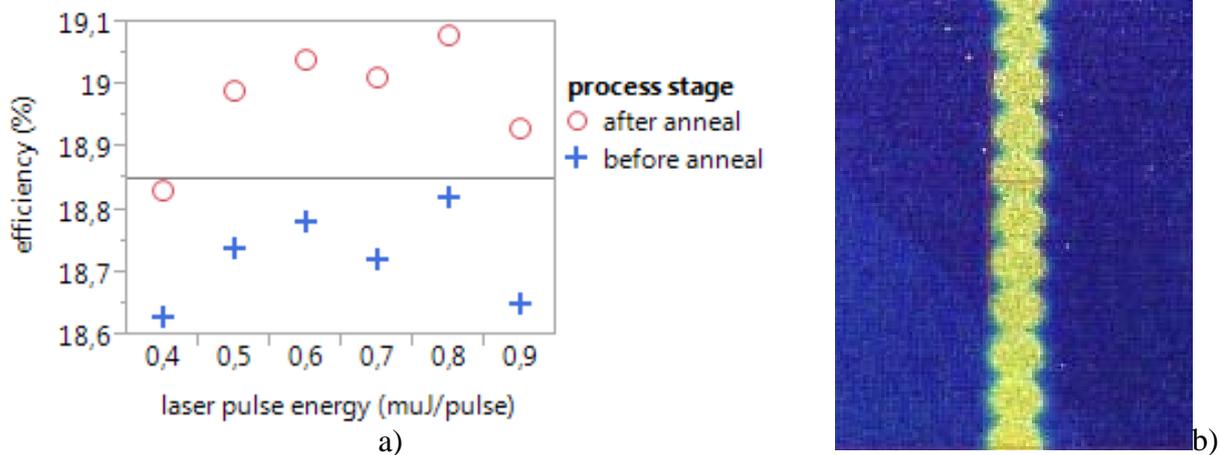
**Fig 3:** a) Sketch of Ni/Cu/Ag plating process flow, b) picture of light-induced plating LIP Cu process bath, c) representative SEM image of plated contact finger on a MCT DWS-Si-wafer. Left: top view of a 22- $\mu\text{m}$  wide finger is shown. Right: cross-section of the same contact finger with a finger height around 10- $\mu\text{m}$  is shown.

### 3 Experimental work

In order to achieve an efficiency increase with plated cells relative to Ag-paste screen printed cells, some processes steps have to be individually optimized [8].

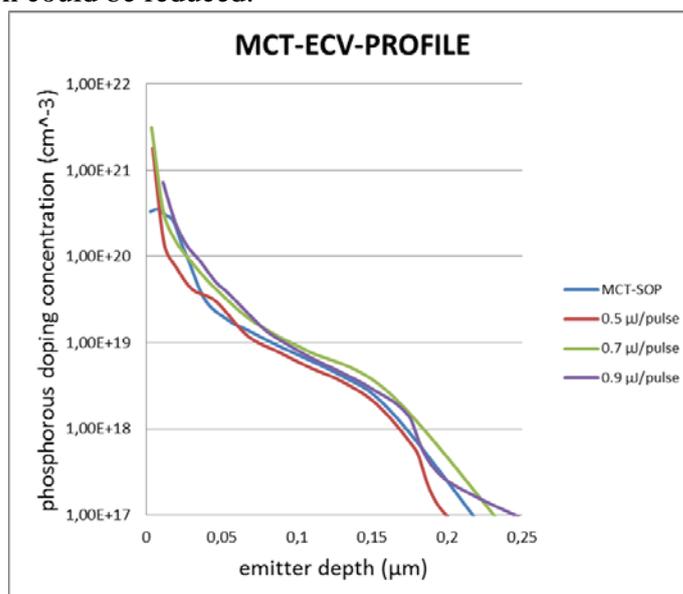
#### 3.1 Laser processing and influences on solar cell performance

First, the laser process was optimized to the MCT wafer surface. The goal is to realize a homogenous opening in the  $\text{SiN}_x$  layer while simultaneously minimizing the creation of a laser-induced damage region (specifically, silicon amorphization) within the emitter's crystal structure. In Figure 4 a), mean efficiency values (5 cells per group) before and after annealing are shown over varied laser pulse energies. A wide process window, between 0.6 to 0.8  $\mu\text{J}/\text{pulse}$ , results in high efficiencies above 19%. No significant difference of the investigated dependency can be distinguished before and after the anneal process, aside from an increase in efficiency by approximately 0.2%. In Figure 4 b), a representative surface image after laser patterning is shown for the optimum laser parameter of 0.8  $\mu\text{J}/\text{pulse}$ .



**Fig 4:** a) mean efficiency values (5 cells per group) before and after annealing over normalized laser power is shown. b) Representative surface image after laser patterning.

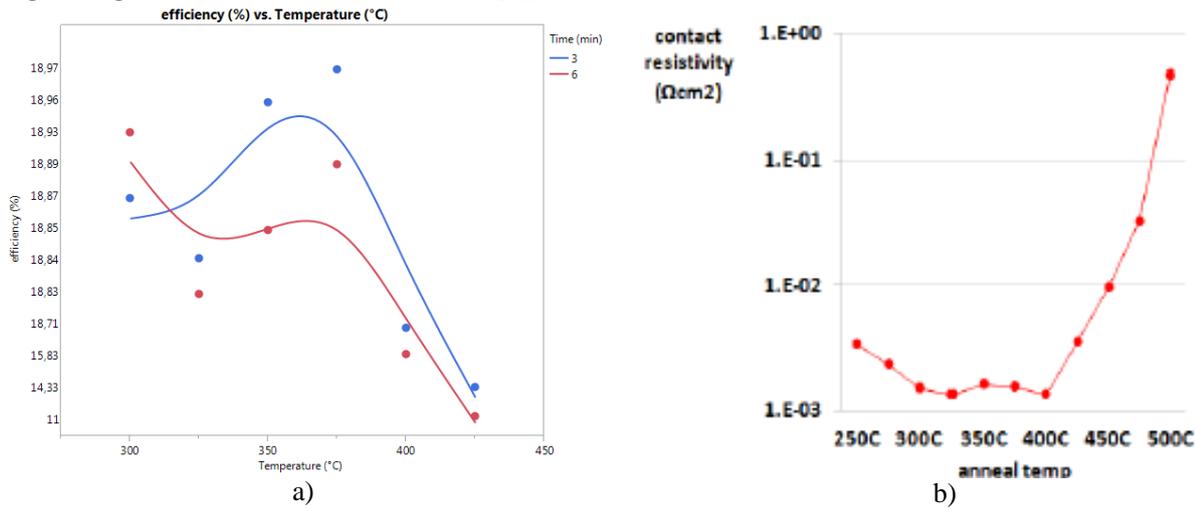
In Figure 5 a comparison of emitter doping profiles before and after laser ablation for three different normalized laser power settings (0.5, 0.7, and 0.9  $\mu\text{J}/\text{pulse}$ ), is presented. The laser process only influences the emitter profile to a depth of approximately 50-nm. The emitter depth below 50-nm is only marginally influenced. Thus, no strong laser damage is induced during laser patterning of MCT mc-Si wafers. However, since the emitter terminates at a shallow depth of approximately 200 nm, and considering that during the annealing process the Ni-layer at the interface forms a Ni-silicide with the emitter layer, it can be reasoned that a deeper junction depth might be beneficial for further efficiency increase. It is important to note that the emitter profile shown in Figure 5 is optimized for Ag-silver paste only and does not reflect the optimum emitter profile for maximizing the solar cell performance for a plated cell. A profile with a reduced surface concentration and a deeper junction would lead to higher efficiencies since the Auger-Recombination could be reduced.



**Fig 5:** Comparison of emitter doping profiles before and after laser ablation for three different normalized laser power setting (0.5, 0.7, and 0.9  $\mu\text{J}/\text{pulse}$ ).

### 3.2 Influence of annealing process

In Figure 6 a), the influence of the annealing temperature on the efficiency is shown for two different annealing durations: 3-min and 6-min. The efficiency is quite stable between 18.8% and 19.0% for annealing temperatures between 300°C and 375°C. For higher temperatures, the efficiency drops rapidly. No significant difference is observed between 3-min and 6-min annealing times. Thus, in the future, a 3-min anneal time will be considered as optimal. In Figure 6 b), the contact resistivity is shown as a function of annealing temperature (3-min). A good match to the I-V data is demonstrated. For temperatures below 400°C, very low contact resistivity values are observed. Above 400°C, the contact resistance rises rapidly. This finding is in good agreement with observation in [9]



**Fig 6:** a) Efficiency over annealing temperature is shown for two different durations 3 and 6 min. b) Contact resistivity over annealing temperature (3 min) is shown.

### 3.3 I-V data comparison of MCT mc-Si solar cells contacted by silver paste screen-printing vs. Ni/Cu/Ag-plating

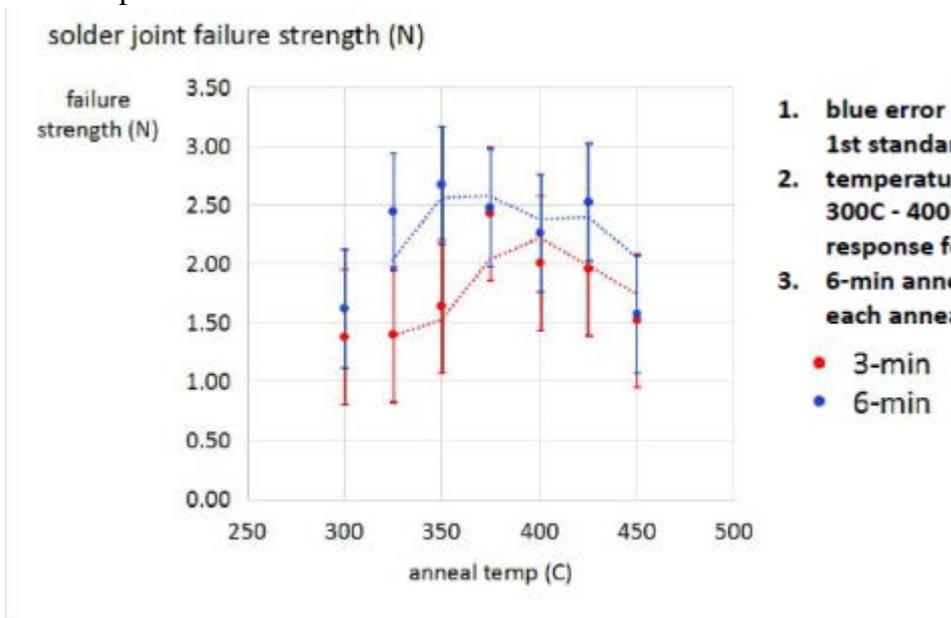
After the initial process development (process ‘base-lining’) cycle, as described in section 3.1 and 3.2, two groups (10 cells each) of cells are fabricated in order to compare the electrical performance of the plated Ni-Cu-Ag contact technology to conventional screen-printed Ag-paste contact technology. The I-V data is shown in Table 1. The efficiency for the plated front contact group is slightly higher. The main reasons for this are a superior short circuit current ( $J_{sc}$ ) and FF. The higher FF is mainly explained by a significantly reduced series resistance ( $R_s$ ) value. Based on the observed  $R_s$  difference, an even higher advantage in FF should be observed [10]. Based on this observed difference, it is reasoned that the plated group has a reduced pseudo fill factor. This finding leads to the conclusion that the diode quality of the plated cells is most likely reduced either by a laser-induced emitter damage, a too-shallow junction, Ni-silicide spiking after anneal, or a combination of the above-mentioned influences.

**Tab. 1:** Overview of I-V data (efficiency,  $V_{oc}$ ,  $J_{sc}$ , FF, and  $R_s$ ) for two different front contact metallization: i) Ni/Cu/Ag plating (after annealing), and ii) Ag-paste screen printing. The group size is 10 cells per group.

Group	$\eta$ (%)	$V_{oc}$ (mV)	$J_{sc}$ (A)	FF (%)	$R_s$ (m $\Omega$ )
Ni/Cu/Ag plated MCT cells	19,04	637	9,11	80,37	1,3
Ag screen printed MCT cells	19,00	642	8,99	80,12	2,1

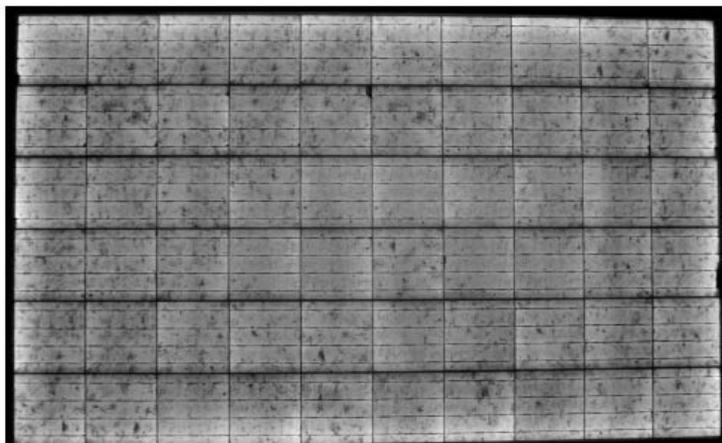
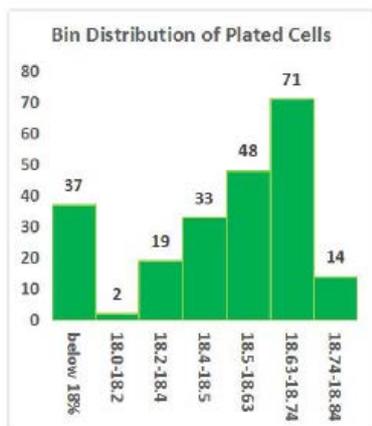
## 4 Module results

Before committing to the fabrication of a full-sized 60-cell module the feasibility of Ni/Cu/Ag-plated MCT mc-Si cells to standard solder technology for interconnecting 12 cells in a string is investigated. In Figure 7, the observed failure pull strength forces, after standard module-production contact tab soldering is performed, are shown. All values were determined under a pull angle of 60°. The contact annealing temperature and time has been varied from 300°C to 450°C and 3-min to 6-min, respectively. Values above 1-N are known to allow for a reliable module production process. Longer annealing times performs slightly better compared to the 3-min anneal, with most values well above 2-N. It is known that the solderability of plated Ag on Cu is much higher than on Ag paste. Optimization of the soldering process will be part of future experiments.



**Fig 7:** Measured failure pull strength force after standard contact tab soldering for module production. All values were determined under a pull angle of 60°. The contact annealing temperature and time has been varied from 300°C to 450°C and 3-min to 6-min, respectively.

After obtaining positive results, as discussed in sec. 3.3 and shown in Fig.7, 225 cells were fabricated (see I-V data distribution in Fig 8a)) and a 60-cell module was subsequently manufactured. The module electro-luminescence (EL) image is shown in Fig.8b). Please note that despite no adaptation of the cell interconnection or the module 'lay-up' processes to the presence of plated bus bar pads, neither cell cracks, nor finger interruptions, nor other typical module failure modes are observed through visible inspection of the EL image. The module I-V data is shown in Figure 8c). The power output accounts 266W. After further optimization loops are completed, modules in the output range of 280-W to 290-W are expected.



a)

b)



Manufacturer:	JKbb-1-0007
Module type:	60p
Module ID code:	
Serial number:	MD
Mon. c.:	POLY
Id. ...:	00000187
Serial. ...:	127.0 mW/(kW/m <sup>2</sup> )
Mode name:	
Method:	Direct
Sample n.:	250
Sample time:	40 us
Meas. ...:	10.000 ms
N <sup>o</sup> measure:	10
Irrad.:	1.000 kW/m <sup>2</sup>
Temp.:	26.1 °C
Avg. Ir.:	1.002 kW/m <sup>2</sup>
Dev. Ir.:	0.000 kW/m <sup>2</sup>
I <sub>sc</sub> :	9.21 A
V <sub>oc</sub> :	36.047 V
EHC:	18.2 %
EHM:	16.2 %
FF:	75.9 %
MPP:	266.942 W
V <sub>mpp</sub> :	30.787 V
I <sub>mpp</sub> :	8.64 A
P <sub>ser</sub> :	0.463 Ohm
P <sub>sh</sub> :	185.59 Ohm

c)

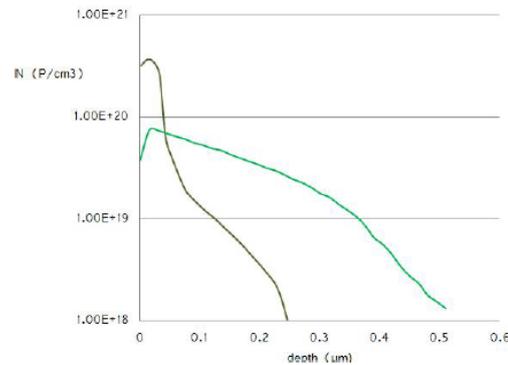
**Fig 8:** a) Efficiency binning distribution of 225 MCT mc-Si solar cells contacted by a plated Ni/Cu/Ag stack. b) EL image and c) I-V data of a very first 60-cell module populated with cells shown in Fig. 8a).

## 4 Conclusion and outlook

Within this publication, we present the case that combining two emerging technologies, specifically, metal catalyzed textured diamond wire saw mc-Si wafering and Ni/Cu/Ag-plated front contact processing, shows very good results, even at the process ‘base-lining’ stage of development. Results from the initial rounds of unit-process optimization, including laser patterning, annealing, and soldering are presented.

A 60-cell module with a power output of 266-W has been presented with fabrication of 280-W to 290-W modules, along with subsequent generation of module reliability data, as ‘next-steps’. Since unit-process development and whole-process integration are in the early stages, it is expected that further optimizations will result in rapid near-term improvement in cell and module performance. As a primary case-in-point, discussed in sec. 3.3, is that the plated cell’s I-V response mainly suffers due to a reduced pFF. A very promising solution for this is

moving away from an emitter profile which is mainly optimized for silver paste's restricted sintering parameters and, thus, limited contacting properties and, instead, developing an optimized emitter profile suited for highly-ohmic Ni/Cu/Ag-plated contacts. The main idea is to reduce the surface doping concentration (reduces Auger-recombination) and increase the junction depth (increased pFF and FF). In Figure 9 the conventional Ag-paste influenced emitter profile and a proto-type plating-optimized emitter are shown. Initial I-V results are imminent.



**Fig 9:** Comparison of n-type Emitter profiles on MCT mc-Si cells: a) current profile which is optimized for Ag-paste screen-printing, b) proto-type emitter profile optimization for Ni/Cu/Ag front-contact formation.

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